

Fig. 1

FIRST PREFERRED EMBODIMENT
PLANAR STRUCTURE OF OPTICAL DISK 1

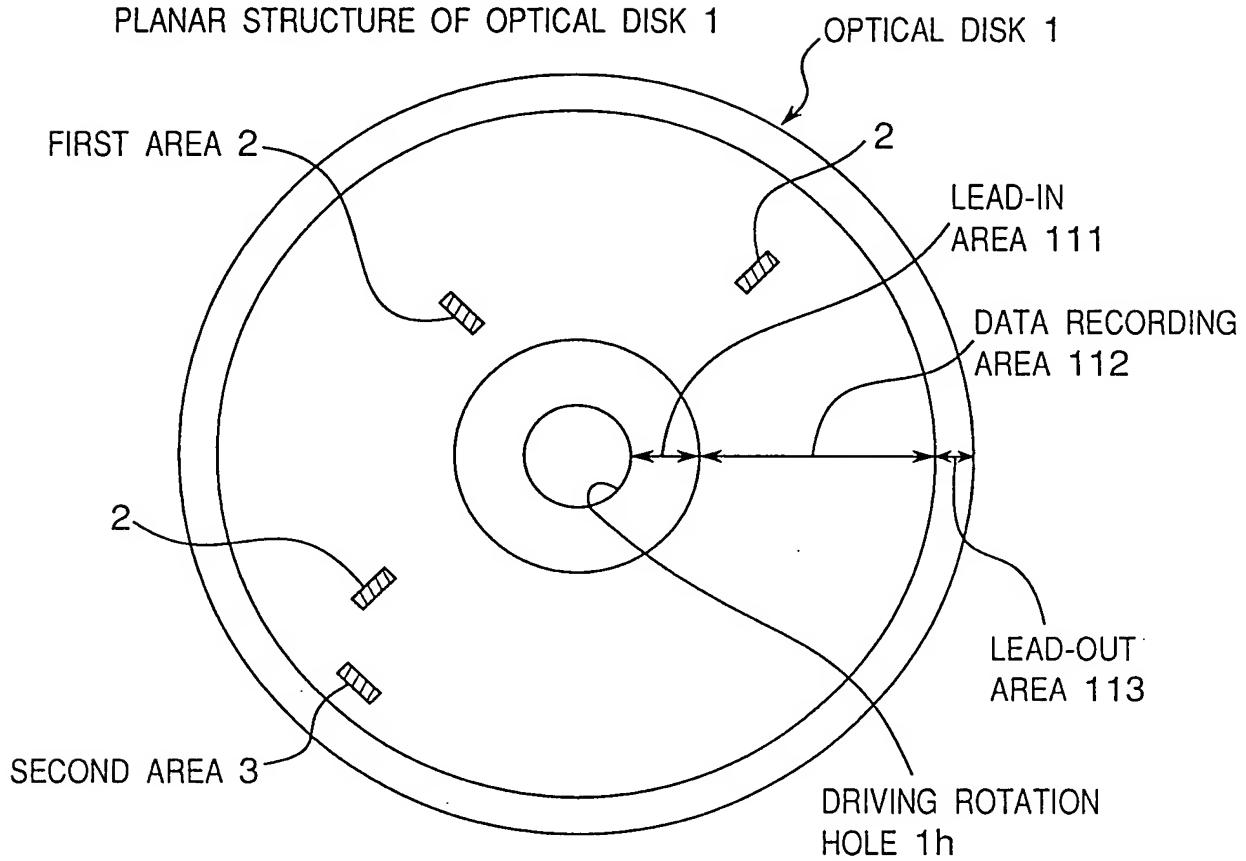


Fig. 2

SECTIONAL STRUCTURE OF OPTICAL DISK 1

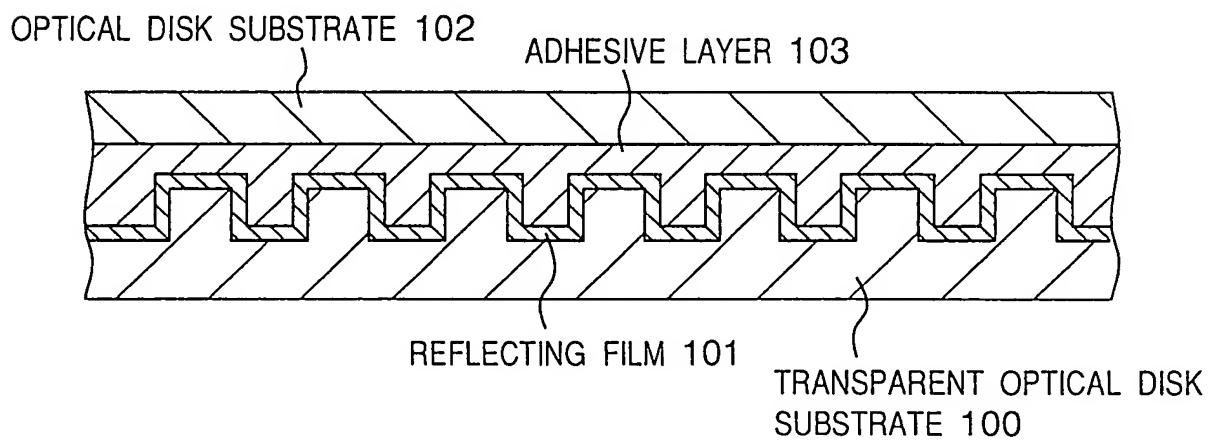


Fig.3

SECTIONAL STRUCTURE OF FIRST AREA 2

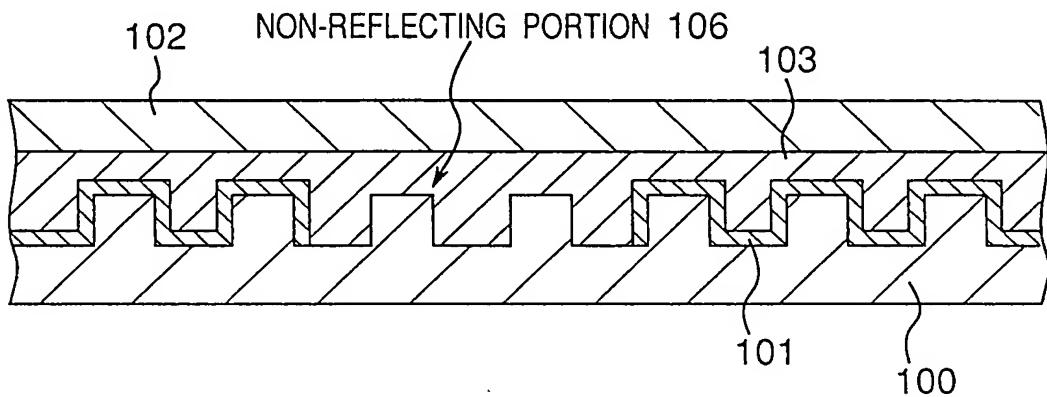


Fig.4

ENLARGED VIEW OF SECOND AREA 3

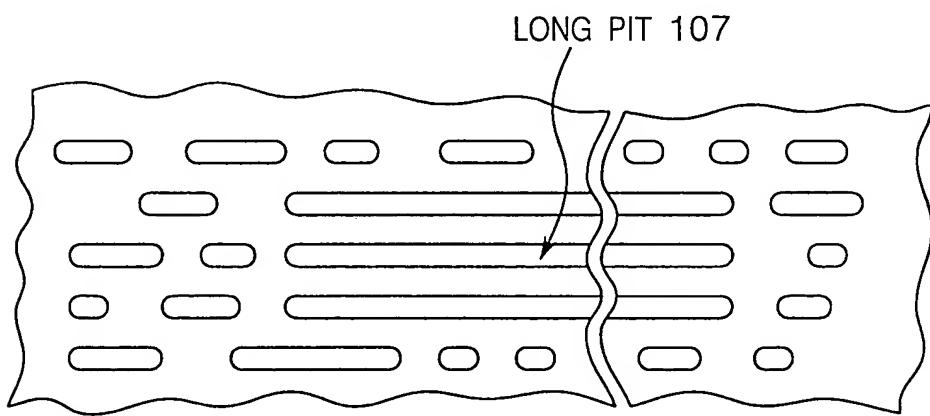


Fig.5

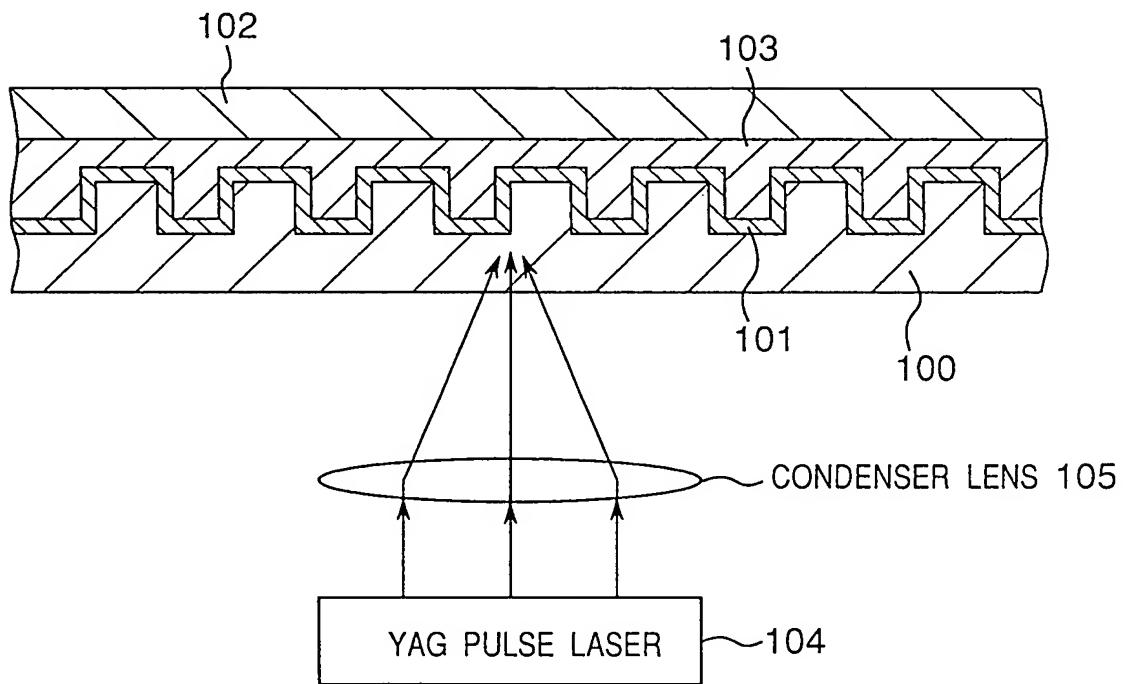


Fig.6A SIGNAL WAVEFORM OF REPRODUCED SIGNAL
AT LOW DEGREE OF MODULATION

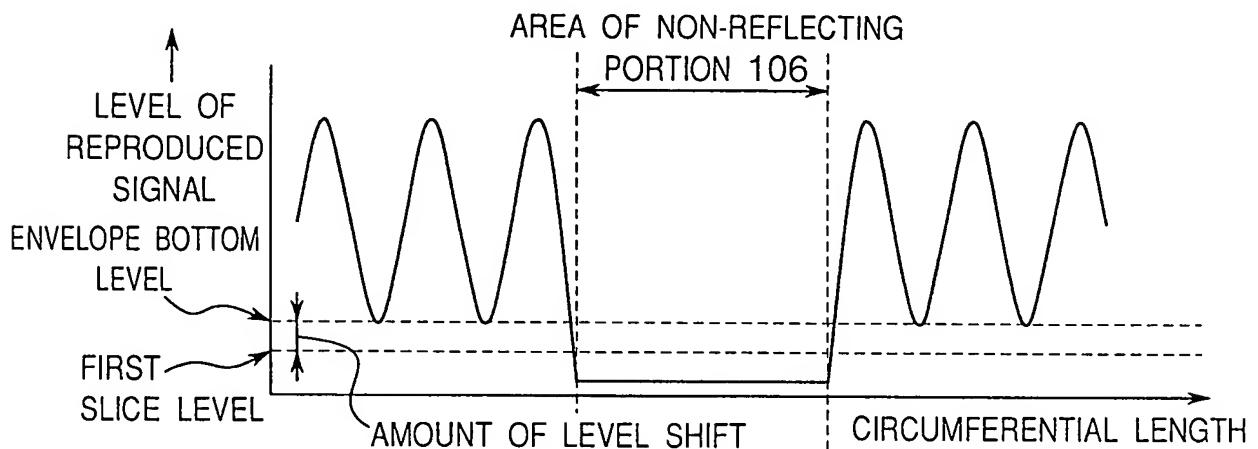


Fig.6B SIGNAL WAVEFORM OF REPRODUCED SIGNAL
AT HIGH DEGREE OF MODULATION

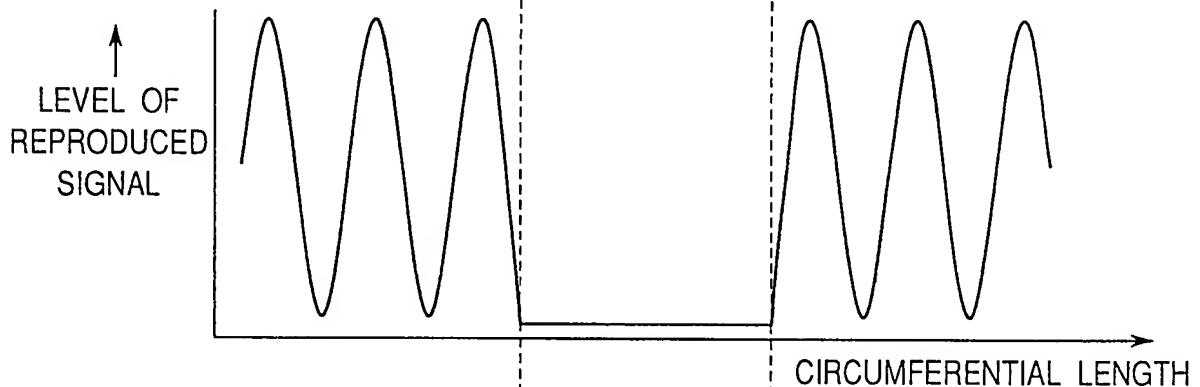


Fig.6C SIGNAL WAVEFORM OF REPRODUCED SIGNAL
AT HIGH DEGREE OF MODULATION AND IN DEFOCUS STATE

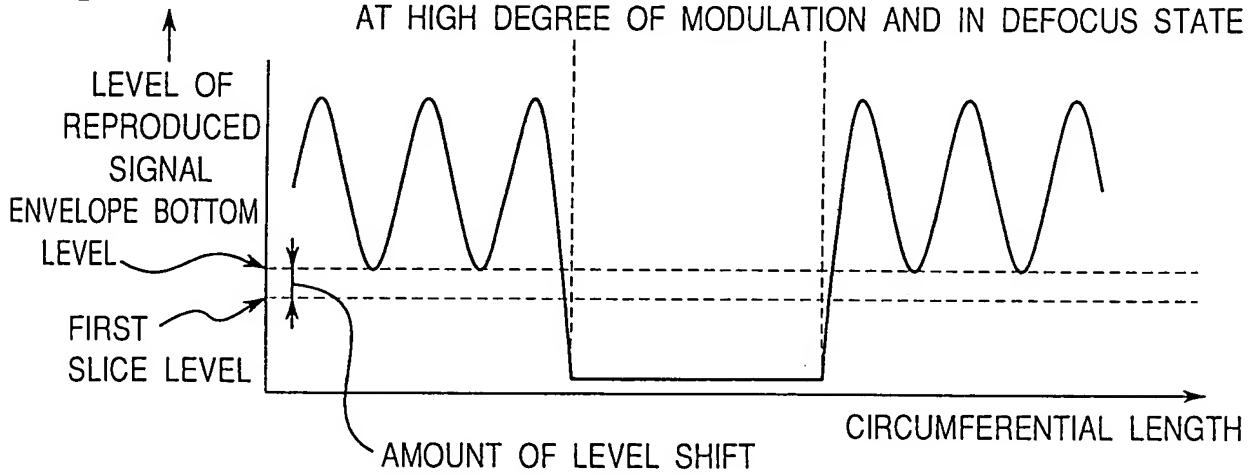


Fig. 7

FIRST AREA DETECTING CIRCUIT 23a

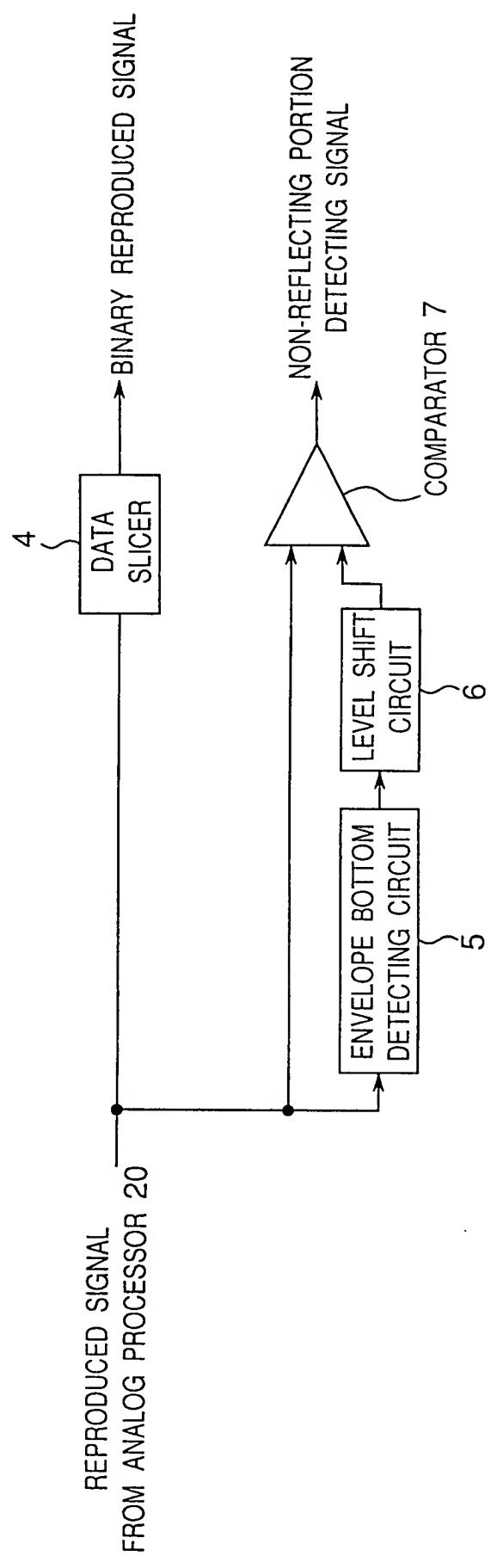


Fig.8

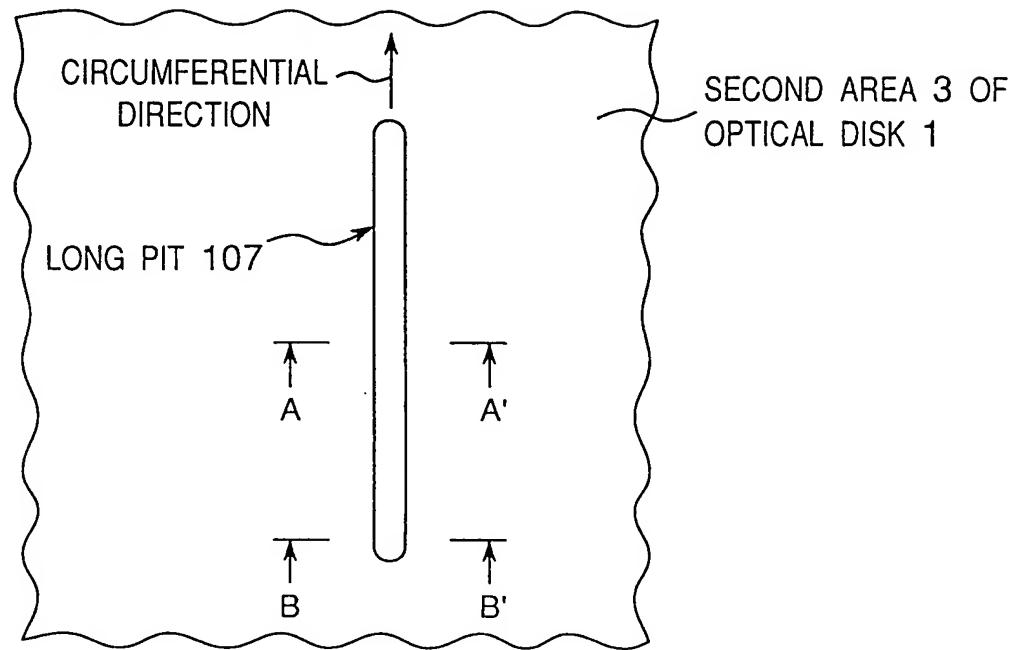


Fig.9A CROSS SECTION IN THE CENTER OF LONG PIT 107
(TAKEN ALONG LINE A-A' OF Fig.8)

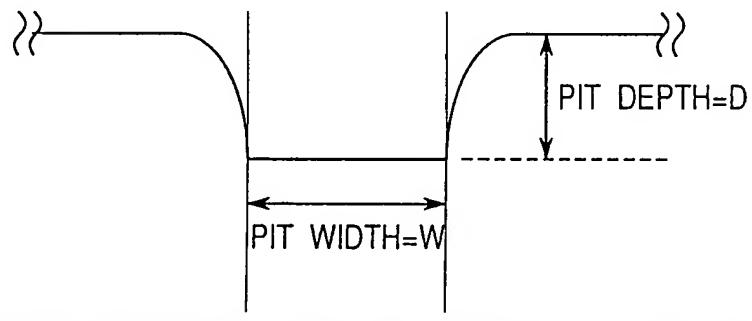


Fig.9B CROSS SECTION IN THE END PORTION OF LONG PIT 107
(TAKEN ALONG LINE B-B' OF Fig.8)

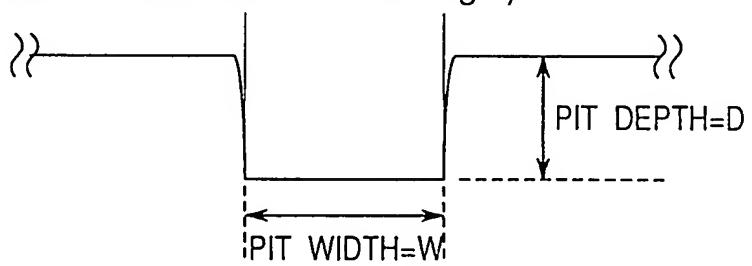


Fig. 10

SECOND AREA DETECTING CIRCUIT 24a

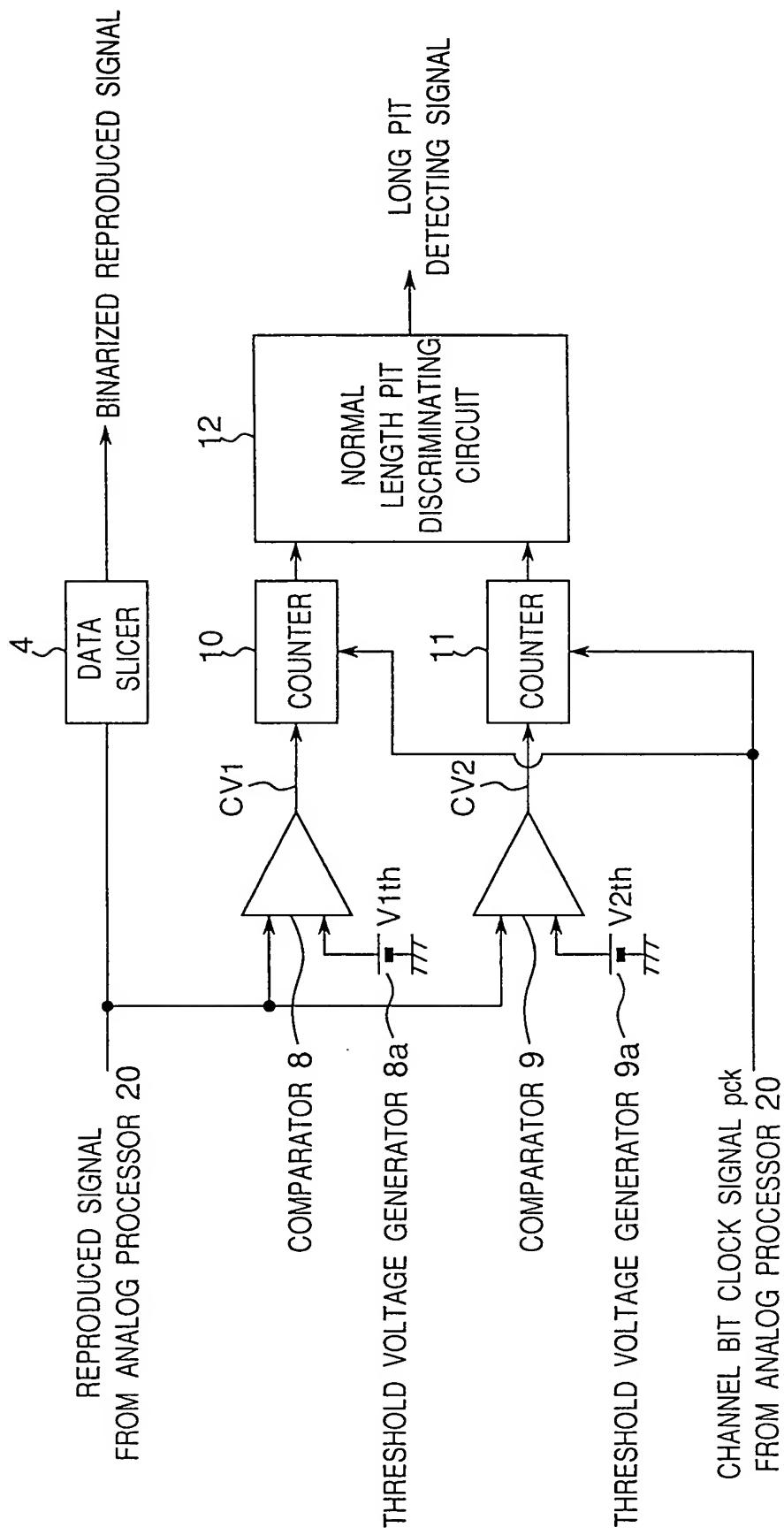


Fig. 11A

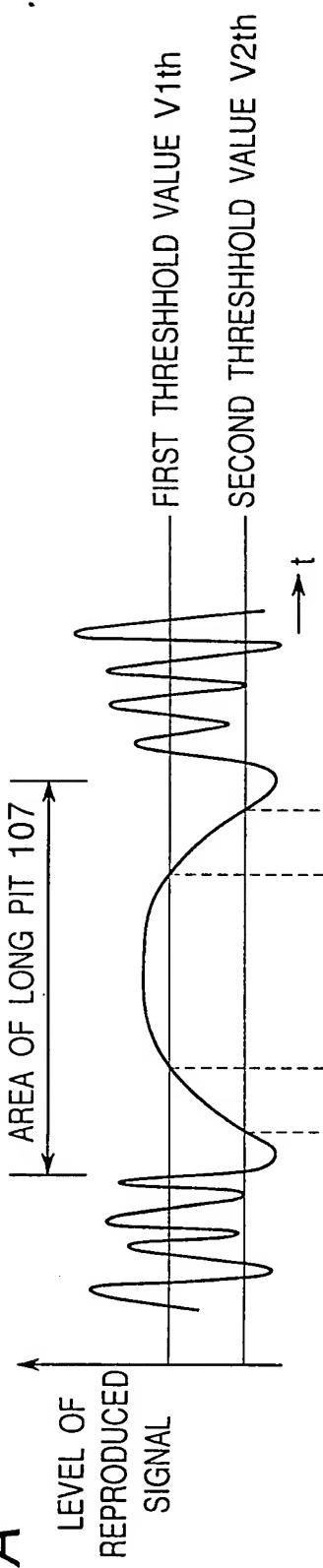


Fig. 11B

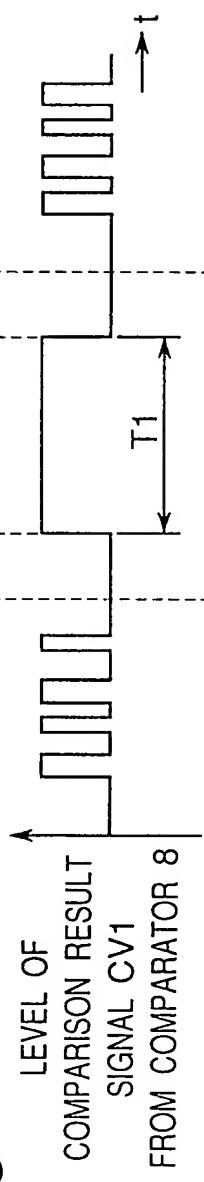


Fig. 11C

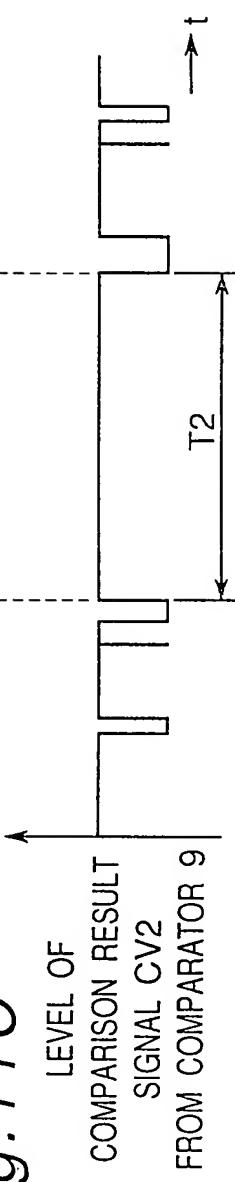


Fig. 12

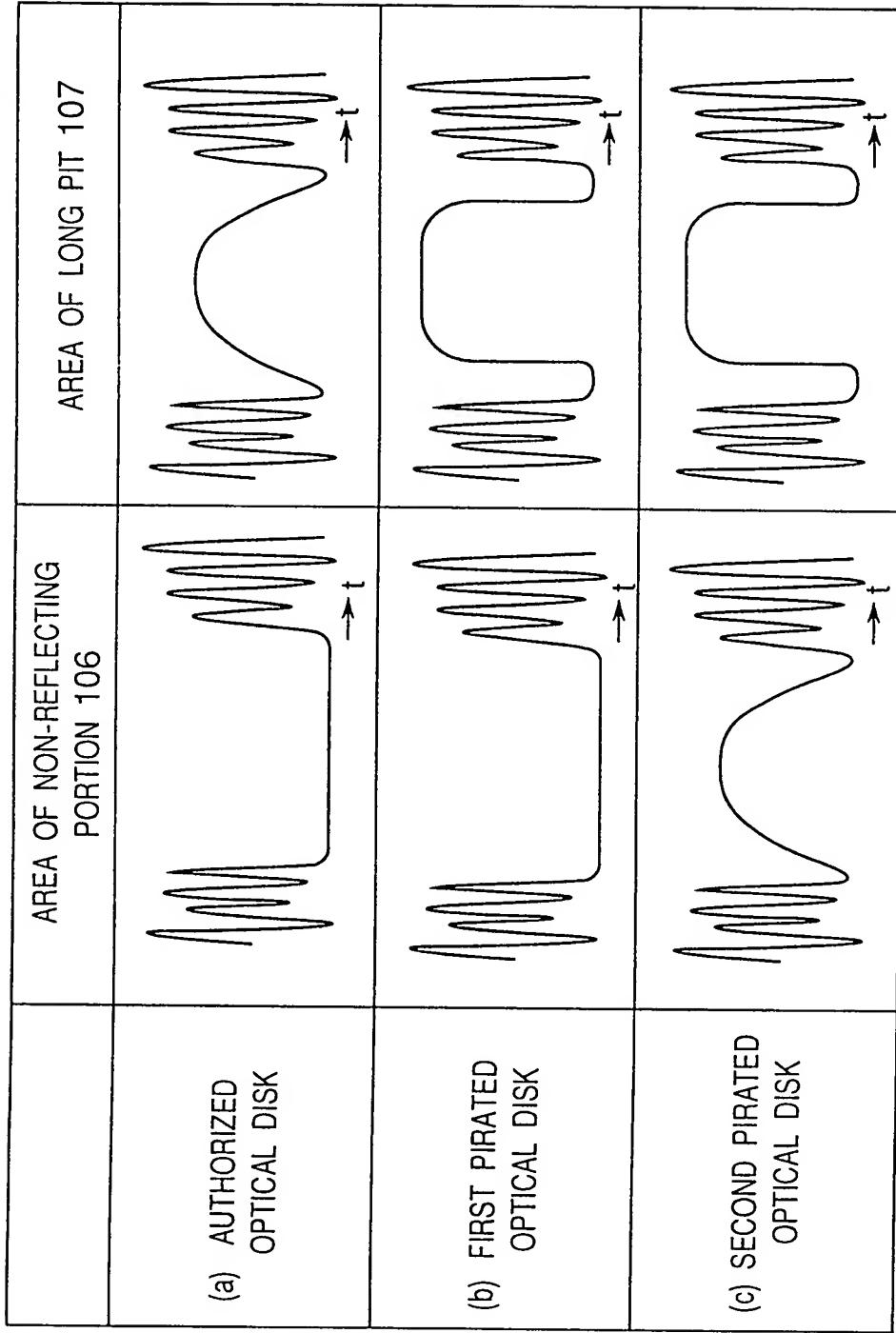


Fig. 13

SECOND PREFERRED EMBODIMENT

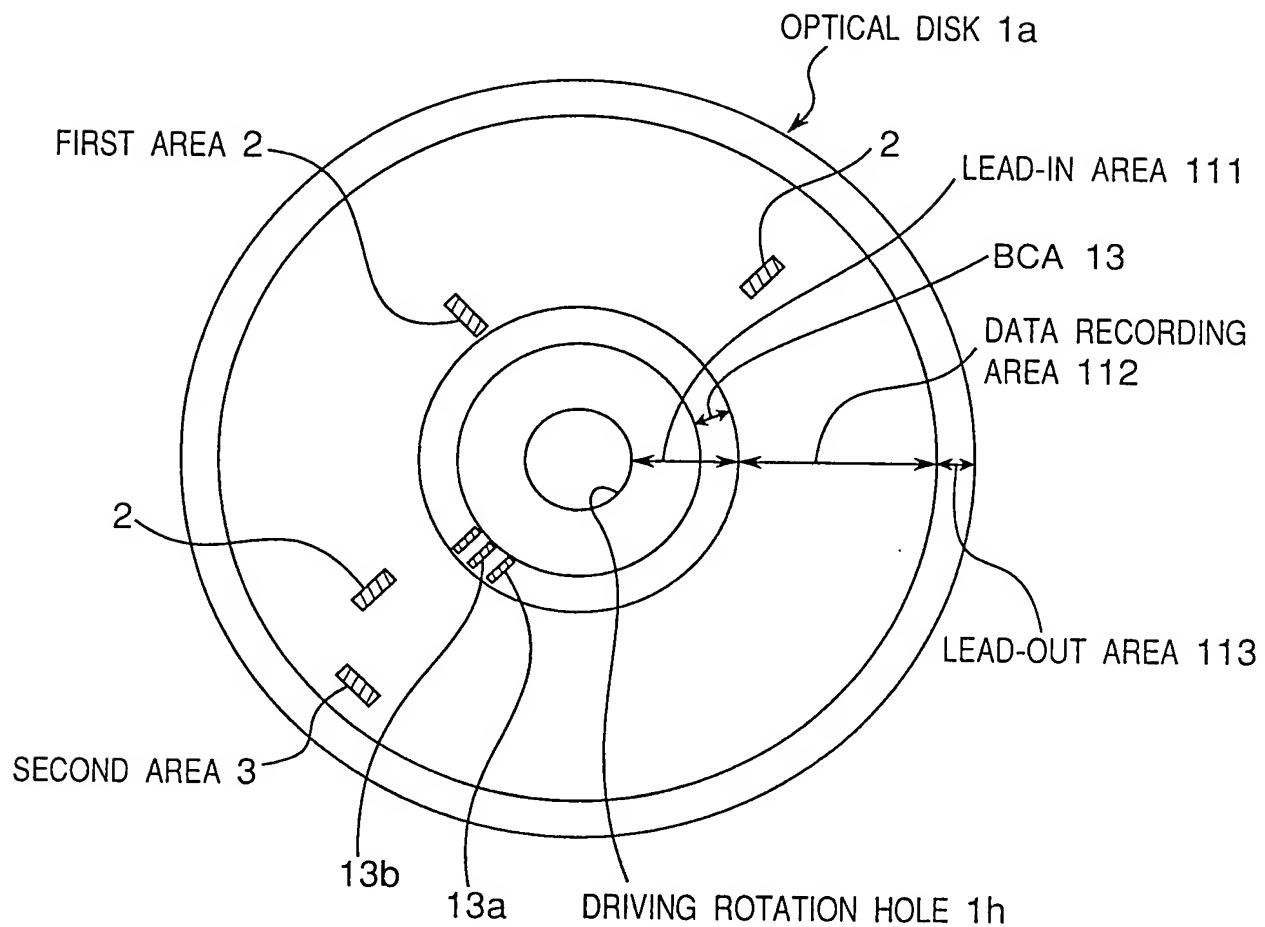


Fig. 14

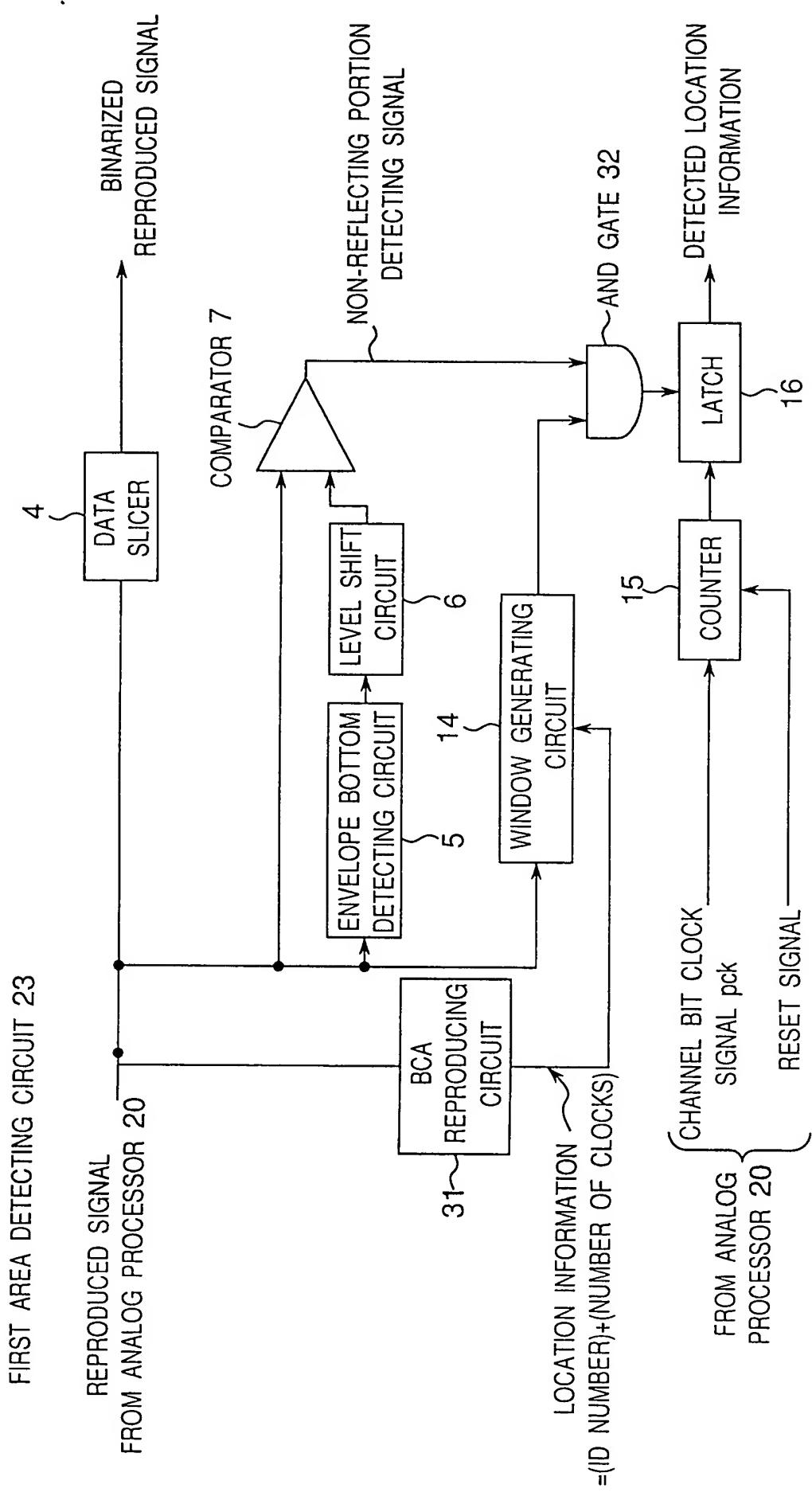
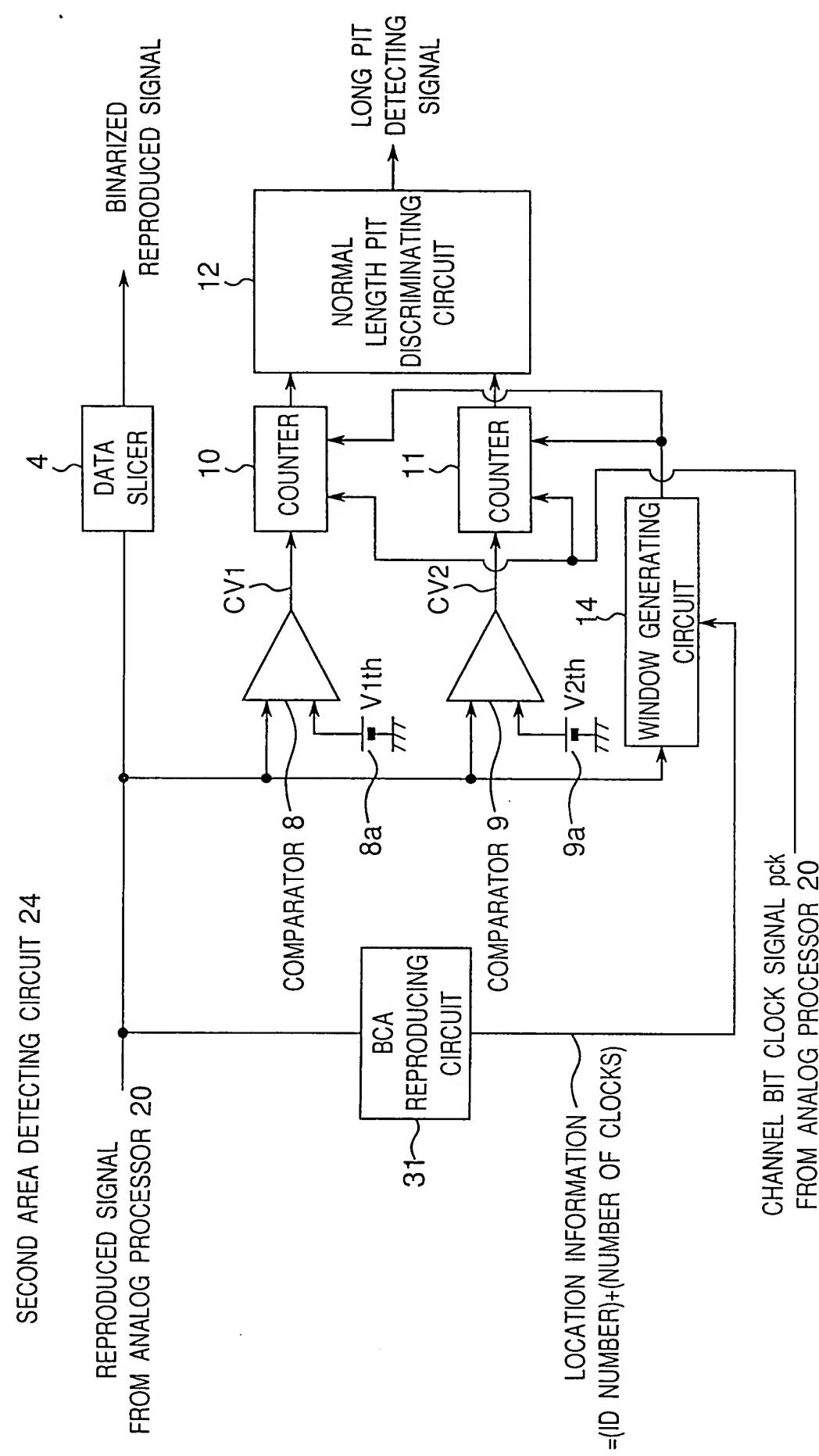


Fig. 15



LOCATION INFORMATION
=(ID NUMBER)+(NUMBER OF CLOCKS)

CHANNEL BIT CLOCK SIGNAL pick
FROM ANALOG PROCESSOR 20

FROM ANALOG PROCESSOR 20

Fig. 16

THIRD PREFERRED EMBODIMENT

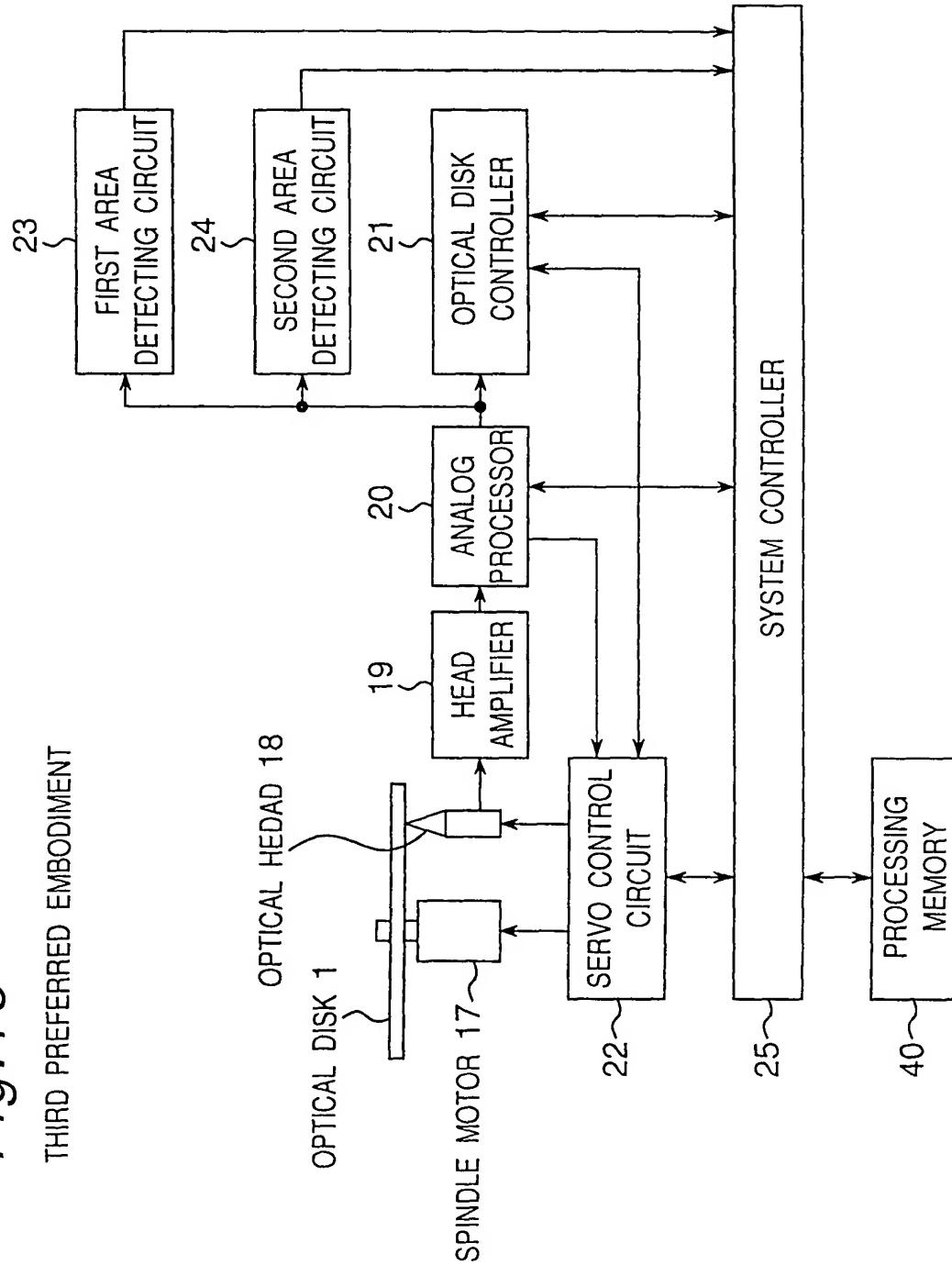


Fig. 17

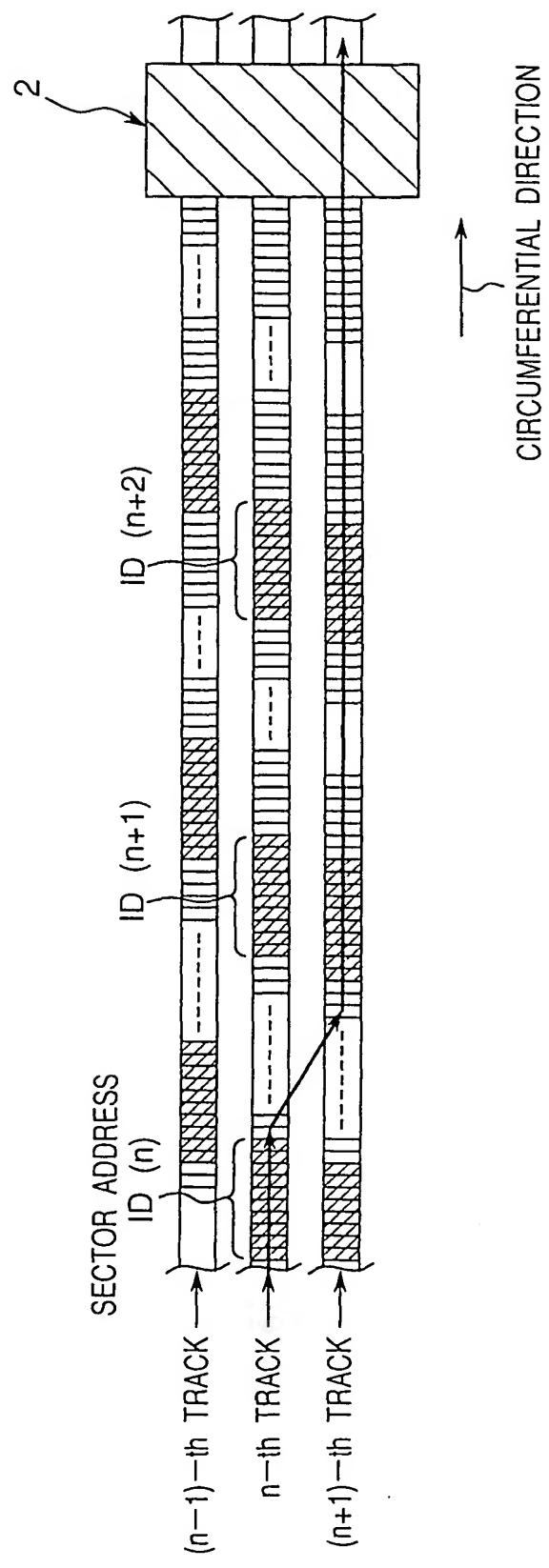


Fig. 18

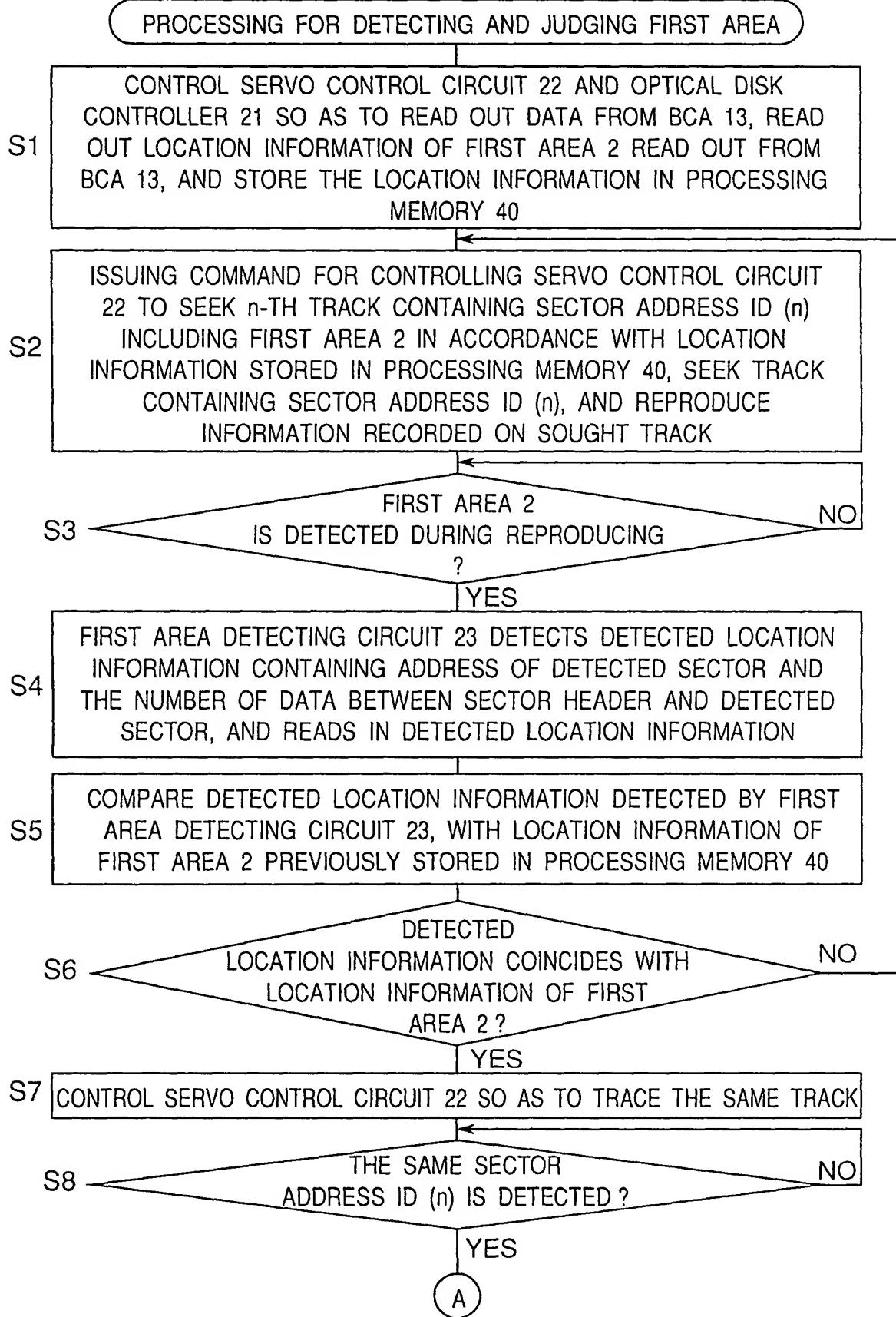


Fig. 19

